

### Fig. 1

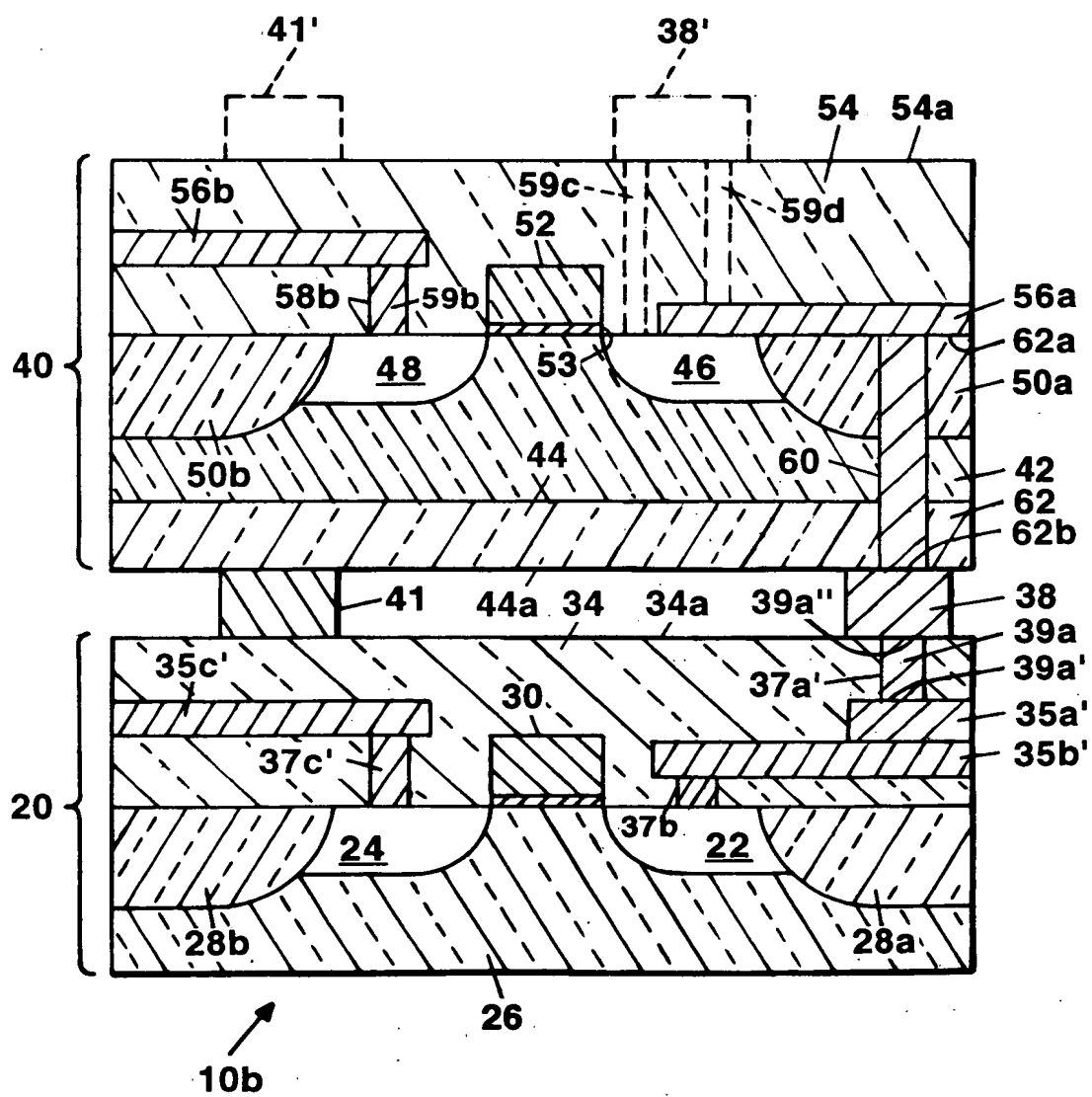


Fig. 2



MULTI-LAYER INTEGRATED SEMICONDUCTOR STRUCTURE

Rafael Reif et al.

Application No. 10/655,854

3/6

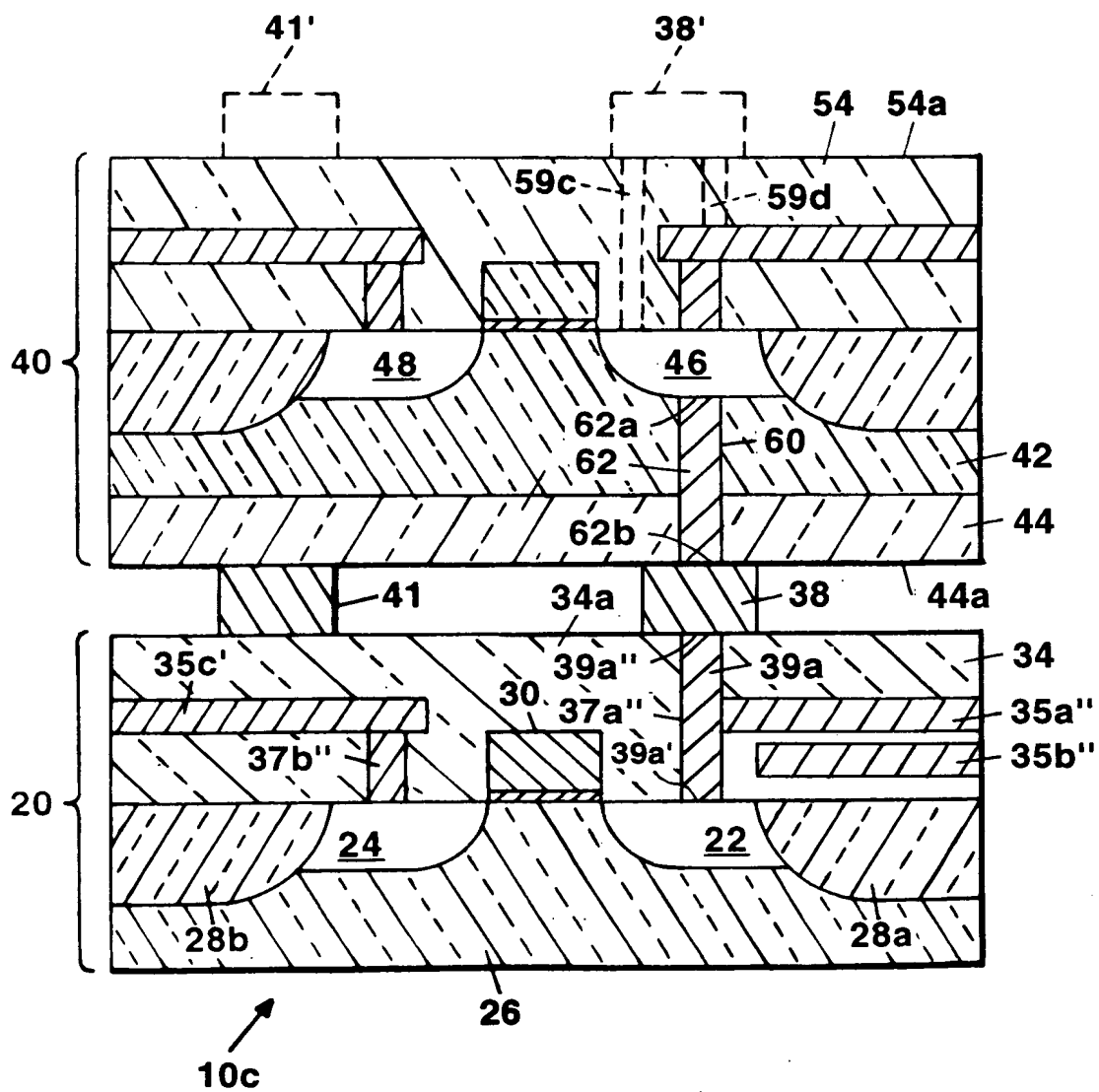
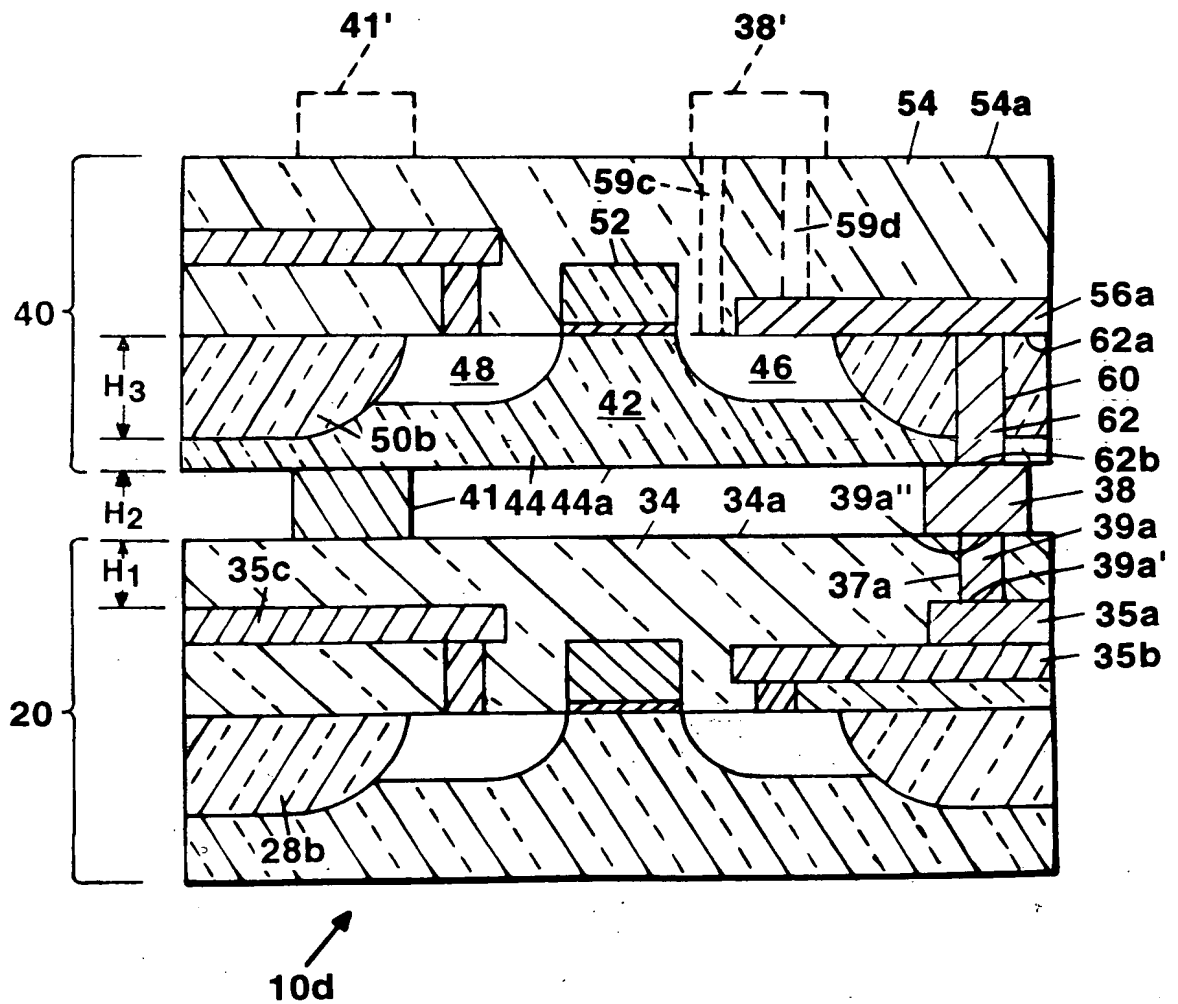


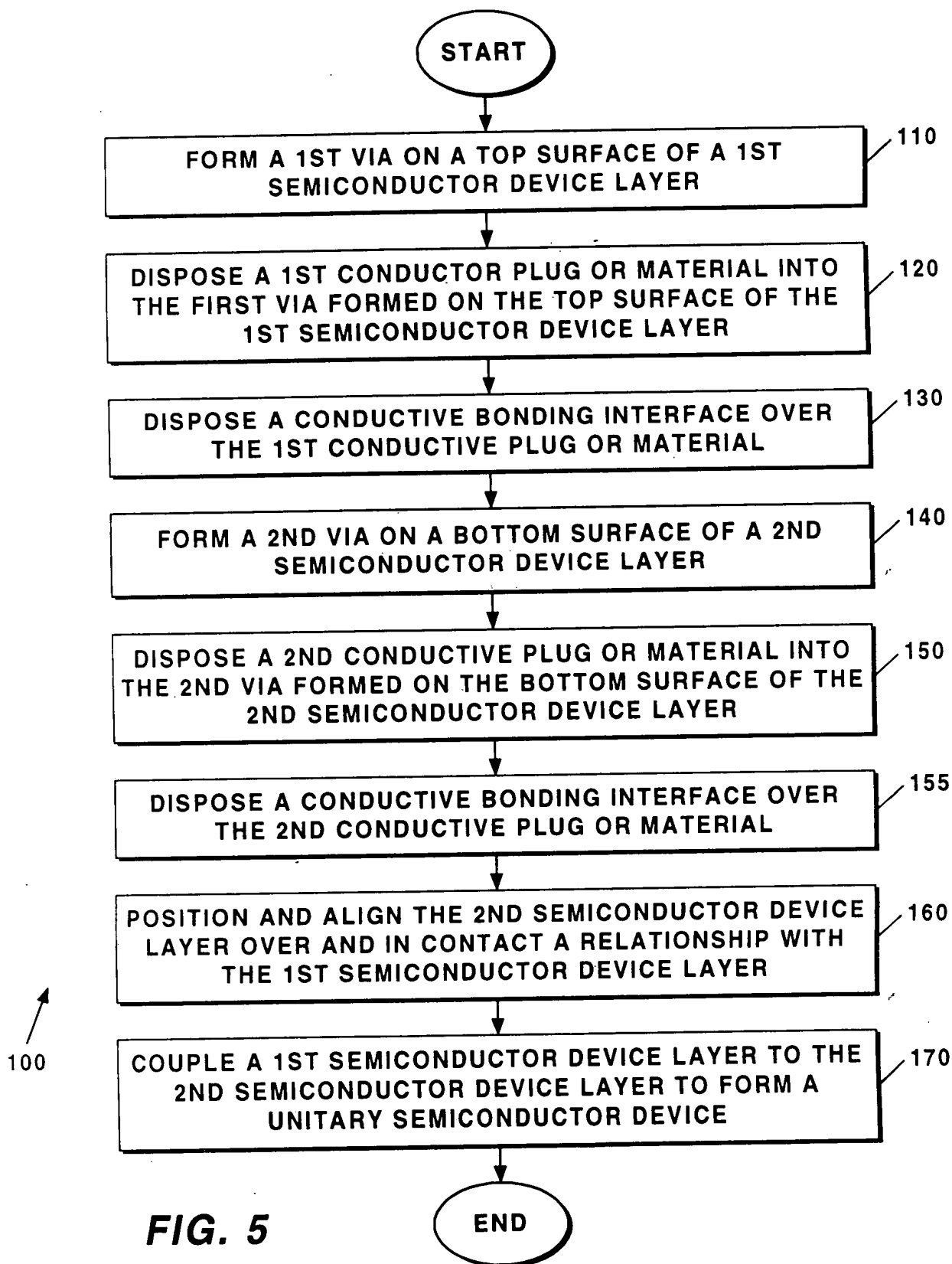
Fig. 3



**Fig. 4**



5/6



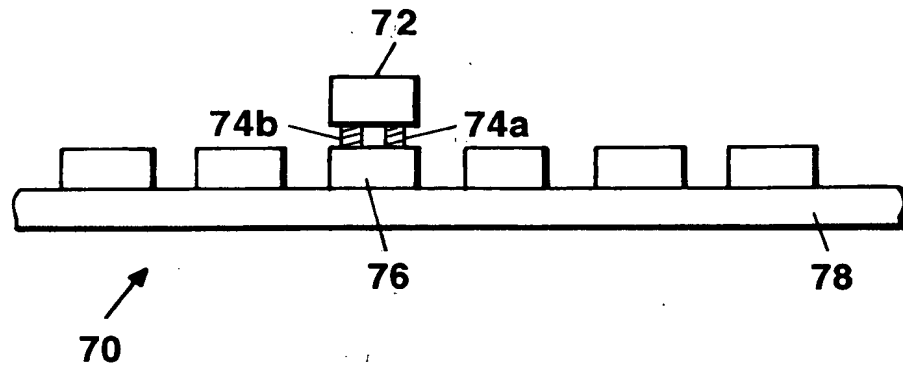


Fig. 6

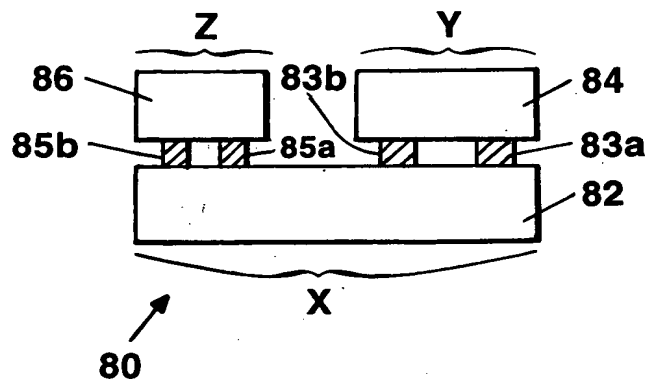


Fig. 7